IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OIPA	In re Application of:					§ .		
DE 100		MOSHE GEFEN ET AL.			§ §			
DEC 1 0 2002	Serial No.:		09/816,459		& & &			
TRADEMARK OF	Filed:	March 26, 2001		01	§ • § • §	Group Art Unit: 2186		
	For:	USING NON-EXECUTABLE MEMORY AS				Attorney Docket: 246/67		
	Examiner: Yamir Encarnacion							
	Commissioner of Patents and Trademarks Washington, DC 20231							
	RESPONSE TRANSMITTAL CUED							
	Sir:	RECEI 2002						
	(1)		Applicant is a: _X_ small entity verified statement attached other than small entity the content of the conten					
		other than small entity						
	(2) The fee for claims 37 CFR1.16(b)-(d) has been calculated as shown below:							
	FOR: TOTAL	CLAIMS	CLAIMS ON FILE 9	AMENDED CLAIMS 22 5	SMALL EN RATE 2 x 9= 2 x 42= TOTAL	TITY FEE	OTHER THAN A SMALL ENTITY - RATE FEE x 18= \$ x 84= \$ TOTAL \$	
	(3)	An ame	ndment	_X_ is filed herew has been file				
	(4) Please charge the extension fee and any other amount required to Deposit Account No. 06-2 A duplicate copy of this form is enclosed. Respectfully submitted,						Account No. 06-2140.	
						1		
1					,	M. Friedman		
						ey for Applicant ration No. 33,883		

Date: December 8, 2002

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8/R 12-13-32

DEC 1 0 2002 ST

In re Applicant:

MOSHE GEFEN ET AL.

Serial No.: 09/816,459

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MEMORY

Examiner:

Yamir Encarnacion

Commissioner of Patents and Trademarks Washington, DC 20231

Group Art Unit: 2186

Attorney

Docket: 246/67

RECEIVED

DEC 1 1 2002

Technology Center 2100

RESPONSE

Sir:

This is in response to the United States Patent and Trademark Office Action mailed October 7, 2001, which response is being made on or before January 7, 2002 and for which no extension fees are due. Please amend the above-identified 12/13/2002 LKINARD 00000002 062140 09816459 application as follows:

01 FC:2201 02 FC:2202

84.00 CH 18.00 CH

In the Specification:

Please replace the paragraph beginning on page 8 line 10 with the following rewritten paragraph:



The 1KB of SRAM is executable and satisfies the requirements of an executable memory (Random Access and standard memory interface).
A CPU or executing entity 12 can therefore execute any location within the range of the SRAM 10. According to the present invention, at any given time, the 1KB of SRAM 10 contains up to 1KB from the